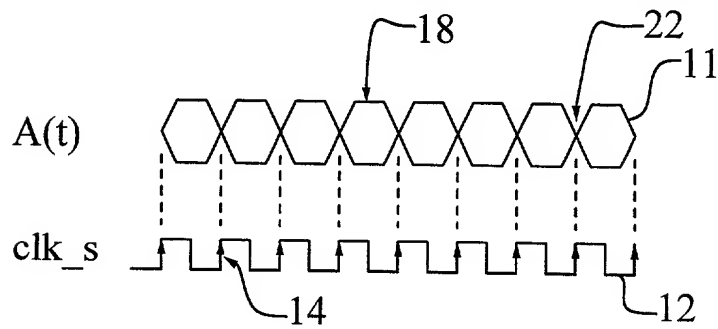


FIG. 1 Prior Art

WRONG PHASE OF SAMPLING CLOCK



CORRECT PHASE OF SAMPLING CLOCK

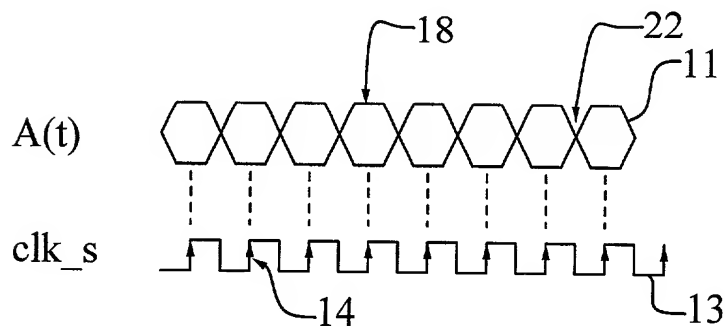


FIG. 2 Prior Art

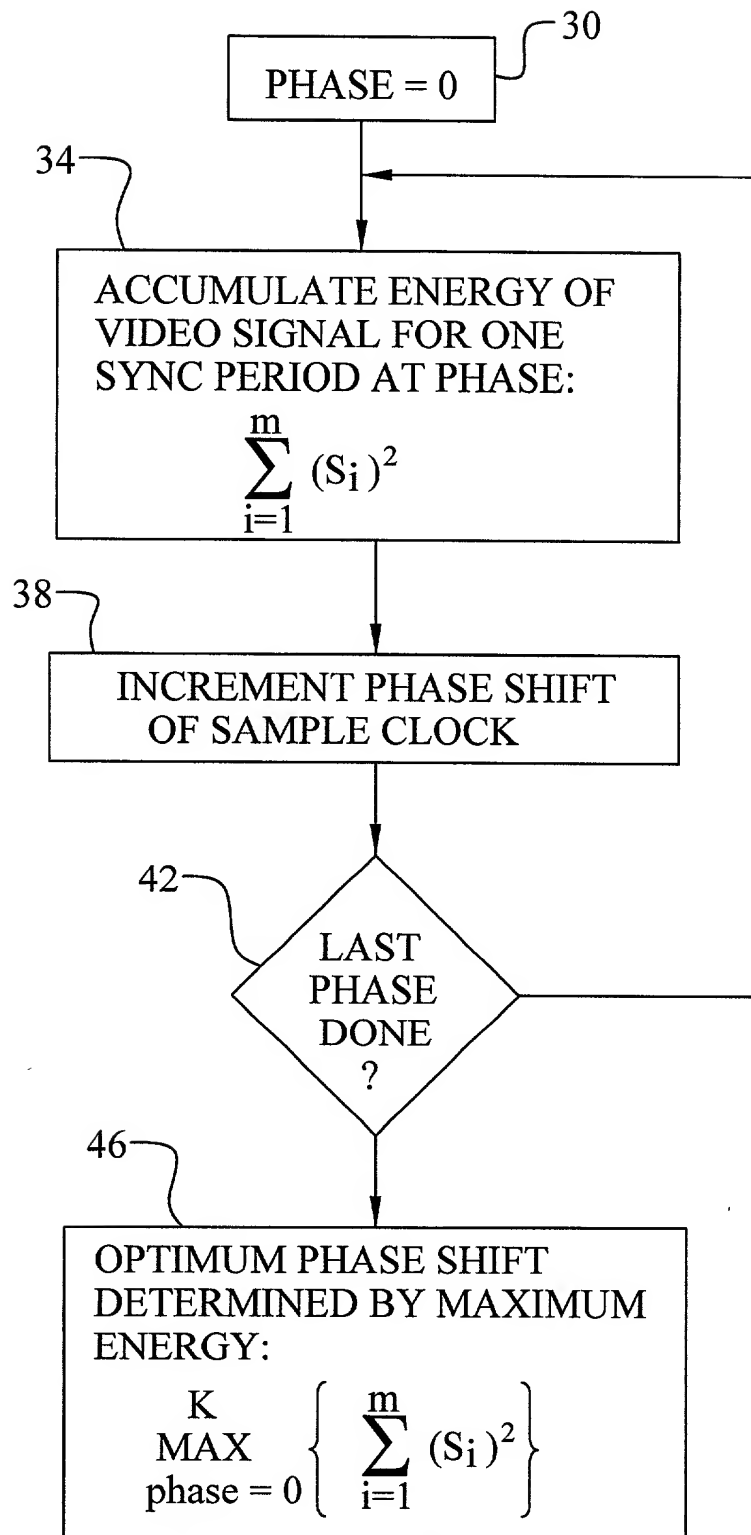


FIG. 3 Prior Art

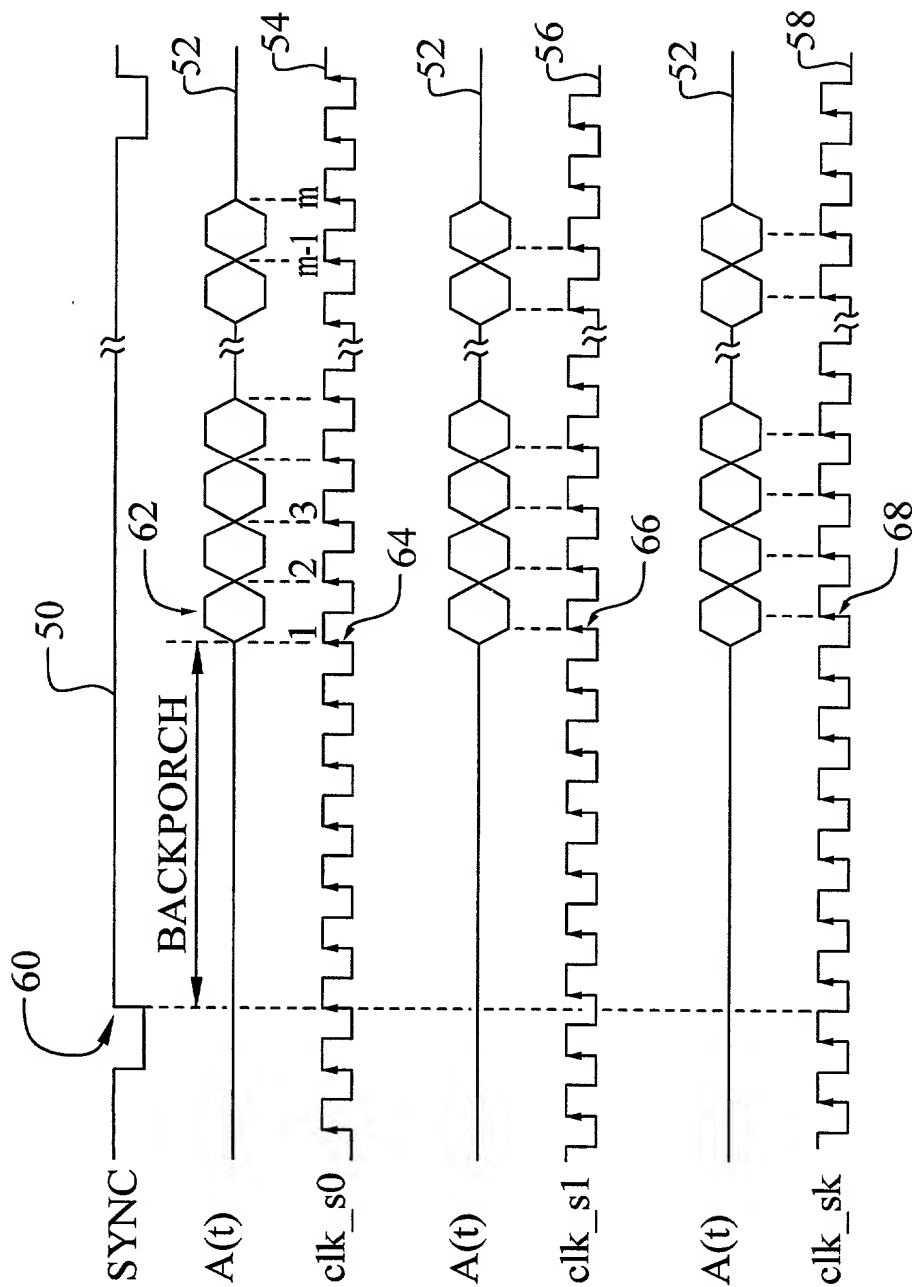


FIG. 4 Prior Art

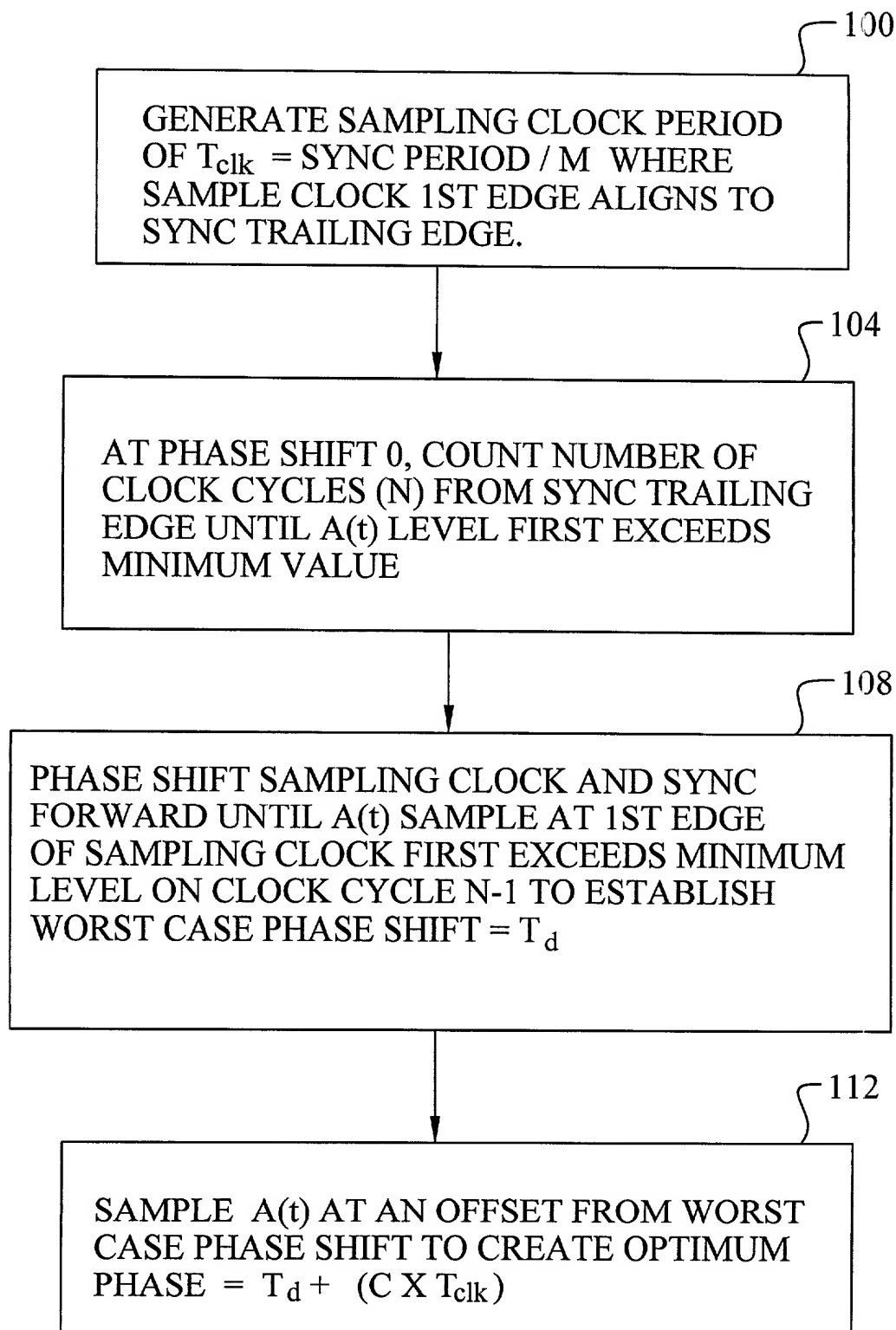


FIG. 5

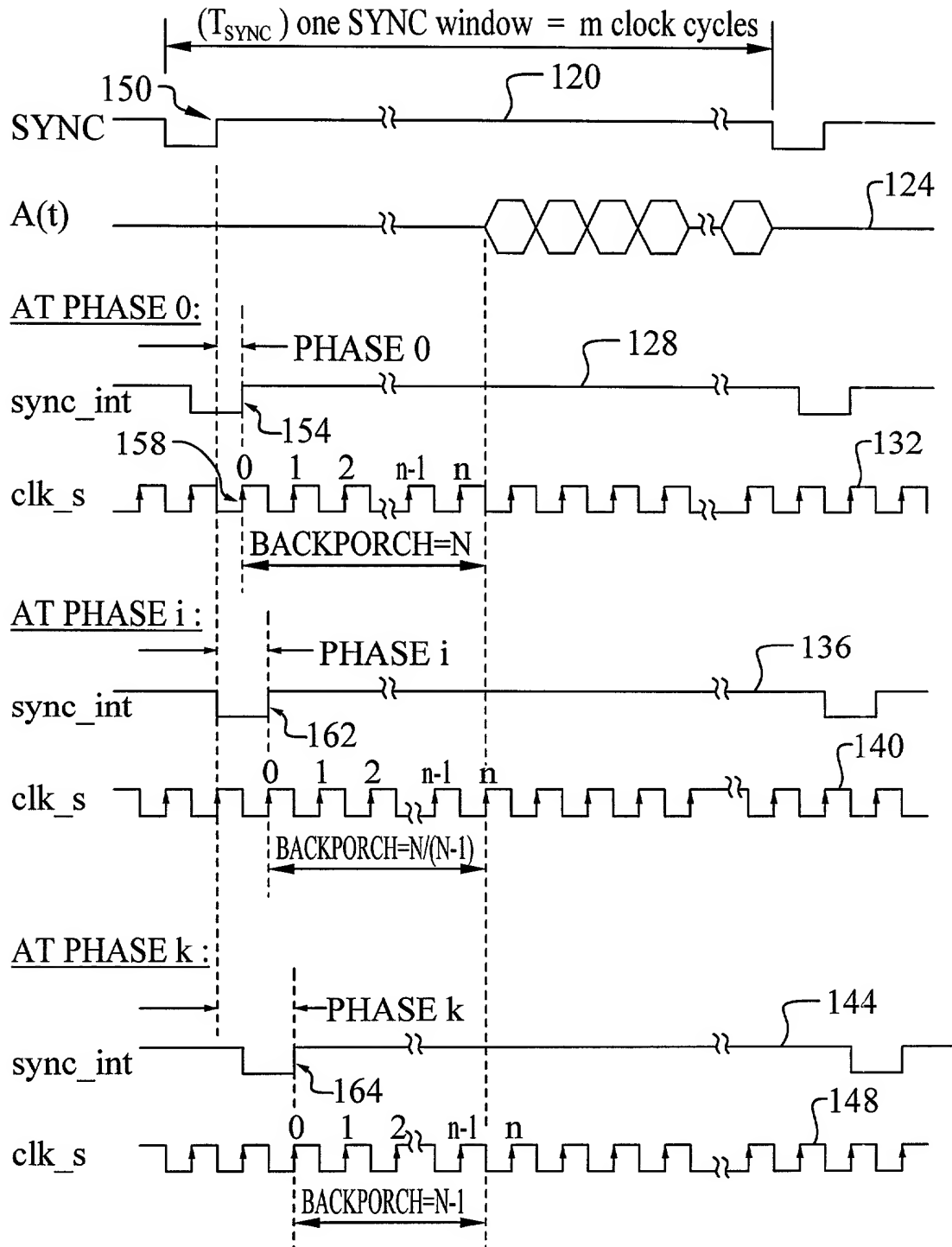


FIG. 6

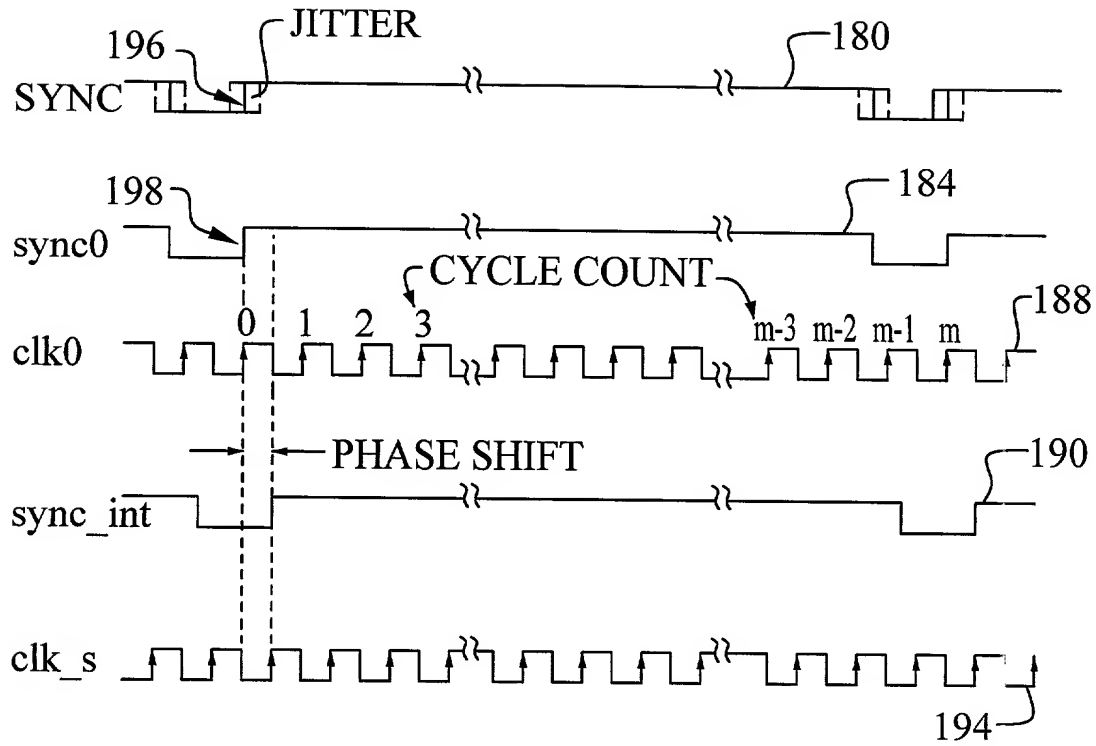


FIG. 7

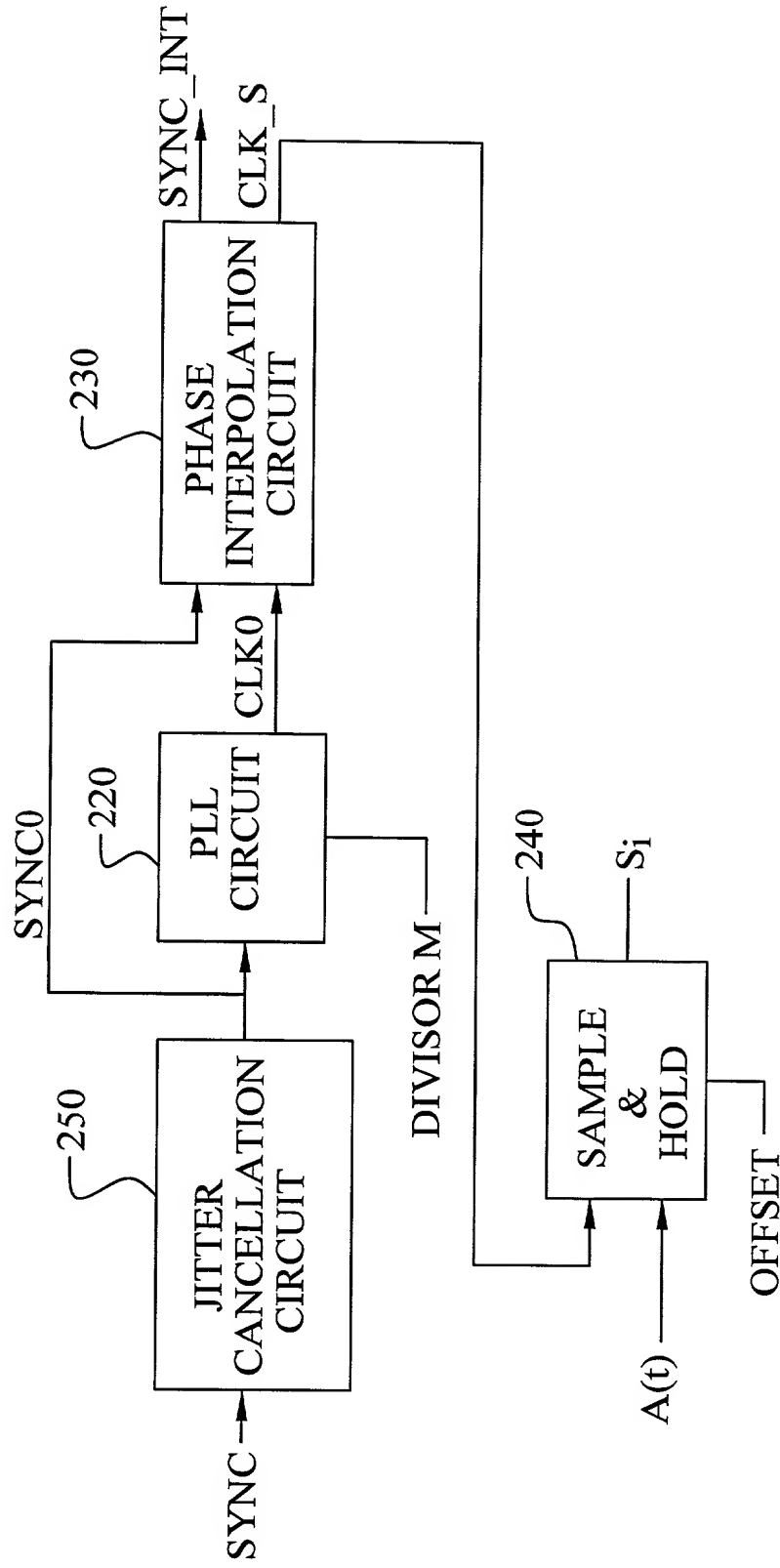


FIG. 8